



IFW AF
2823
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Hurkx et al.	Examiner:	Maldonado, J.
Application No.:	09/854,403	Group Art Unit:	2823
Filed:	May 11, 2001	Docket No.:	PHN 16,741A (VLSI.411PA)
Title:	Semiconductor Device with a Bipolar Transistor, and Method of Manufacturing Such a Device		

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 28, 2004.

By: Erin M. Nichols

Erin M. Nichols

OFFICE ACTION RESPONSE AFTER FINAL

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the final Office Action dated June 24, 2004, please consider the following remarks beginning on page 4. The pending claims are presented on pages 2-3. No amendments to the claims are presented.